

WHAT IS CLAIMED IS:

1. A liquid crystal display having liquid crystal sandwiched by
a pair of substrates having electrodes for driving the liquid
crystal based on a liquid crystal control driving signal for R
light, a liquid crystal control driving signal for G light, and a
liquid crystal control driving signal for B light to control
transmittance of R light components, G light components, and B
light components for color display,
10 a driving voltage for application to the liquid crystal
being set independently for R display, G display, and B display.

2. A liquid crystal display according to claim 1, wherein
an upper limit value of a range for the driving voltage is
set independently for R light, G light, and B light.
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3. A liquid crystal display according to claim 1, wherein
the liquid crystal control driving signal for R light, the
liquid crystal control driving signal for G light, and the liquid
crystal control driving signal for B light are separately
20 subjected gamma correction based on transmittance characteristics
of the R light components, the G light components, and the b
light components.

25 4. A liquid crystal display according to claim 1, wherein
the pair of substrates includes a first substrate,
electrodes for driving the liquid crystal formed on the

first substrate include a plurality of pixel electrodes arranged in matrix thereon; and

the plurality of pixel electrodes are connected to corresponding poly-Si thin film transistors each using a poly-Si 5 layer formed at a low temperature for an active layer.

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3. An electrically controlled birefringence type liquid crystal display having liquid crystal sandwiched by a pair of substrates having electrodes for driving the liquid crystal based on a liquid crystal control driving signal for R light, a liquid crystal control driving signal for G light, and a liquid crystal control driving signal for B light to control transmittance of R light components, G light components, and B light components for color display,

a driving voltage for application to the liquid crystal being set independently for R display, G display, and B display.

6. A liquid crystal display according to claim 5, wherein an upper limit value of a range for the driving voltage is 20 set independently for R light, G light, and B light.

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7. A liquid crystal display according to claim 5, wherein the liquid crystal control driving signal for R light, the liquid crystal control driving signal for G light, and the liquid crystal control driving signal for B light are separately subjected gamma correction based on transmittance characteristics of the R light components, the G light components, and the B

~~light components.~~

8. A liquid crystal display according to claim 5, wherein
the pair of substrates includes a first substrate,
5 electrodes for driving the liquid crystal formed on the
first substrate include a plurality of pixel electrodes arranged
in matrix thereon; and

the plurality of pixel electrodes are connected to
corresponding poly-Si thin film transistors each using a poly-Si
10 layer formed at a low temperature for an active layer.

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